

Amendment

Amendment to Claims

Claim 1 (previously presented): A system comprising:

a peripheral device adapted to define a plurality of device functions accessible through a data interface with a data bus;

a first processing system adapted to communicate with a first device function defined by the peripheral device through the data interface; and

a second processing system adapted to communicate with a second device function defined by the peripheral device through the data interface.

Claim 2 (original): The system of claim 1, wherein the first processing system comprises logic to enumerate each device function associated with an I/O channel.

Claim 3 (original): The system of claim 2, wherein the first device function comprises logic to communicate with a RAID channel.

Claim 4 (original): The system of claim 1, wherein the second processing system is coupled to the data bus through a bridge and the first processing system is a peripheral device

Claim 5 (previously presented): The system of claim 1, wherein the first processing system comprises logic to cause the peripheral device to conceal one or more device functions from the second processing system while enabling the second processing system to communicate with at least one unconcealed device function defined by the peripheral device.

Claim 6 (currently amended): The system of claim 5, wherein the first processing system comprises:

logic to enumerate a first device function defined by the peripheral device; and
logic to set information in a configuration header maintained at the peripheral device to conceal the first device function from the second processing system while enabling the first processing system to communicate with the first device function.

Claim 7 (original): The system of claim 5, wherein the system further comprises a bridge coupled to the peripheral device through a secondary bus, and wherein the bridge comprises logic to initiate execution of an enumeration process by the first processing system prior to completion of an enumeration process by the second processing system.

Claim 8 (original): The system of claim 5, wherein the first processing system comprises logic to transmit a signal to the peripheral device to inhibit enumeration of the peripheral device by the second processing system.

Claim 9 (previously presented): A method comprising:

initiating a first enumeration procedure at a first processing system to enumerate a first device function defined by a peripheral device coupled to a data interface of a data bus; and

initiating a second enumeration procedure at a second processing system to enumerate a second device function defined by the peripheral device.

Claim 10 (original): The method of claim 9, the method further comprising enumerating at least one device function associated with an I/O channel.

Claim 11 (original): The method of claim 10, wherein the device function associated with the I/O channel comprises logic to communicate with a RAID channel.

Claim 12 (original): The method of claim 9, wherein the second processing system is coupled to the data bus through a bridge and the first processing system is a peripheral device

Claim 13 (previously presented): The method of claim 9, the method further comprising causing the peripheral device to conceal one or more device functions from the second processing system while enabling the second processing system to communicate with at least one unconcealed device function defined by the peripheral device.

Claim 14 (currently amended): The method of claim 13, wherein the method further comprises:

enumerating a first device function defined by the peripheral device; and
setting information in a configuration header maintained at the peripheral device to conceal the first device function from the second processing system while enabling the first processing system to communicate with the first device function.

Claim 15 (original): The method of claim 13, wherein the method further comprises initiating execution of an enumeration process by the first processing system prior to completion of an enumeration process by the second processing system.

Claim 16 (original): The method of claim 13, wherein the method further comprises transmitting a signal to the peripheral device to inhibit enumeration of the peripheral device by the second processing system.

Claim 17 (previously presented): An article comprising:
storage medium comprising machine-readable instructions stored thereon for:
initiating a first enumeration procedure at a first processing system to enumerate a first device function defined by a peripheral device coupled to a data interface of a data bus, the peripheral device defining a plurality of device functions; and
initiating a bus transaction on the data bus to cause the first device function to be concealed from subsequent enumeration procedures while enabling at least one other

device function defined by the peripheral device to be enumerated by a subsequent enumeration procedure.

Claim 18 (original): The article of claim 17, wherein the storage medium further comprises machine-readable instructions stored thereon for enumerating the first device function as an I/O channel.

Claim 19 (original): The article of claim 18, wherein the device function associated with the I/O channel comprises logic to communicate with a RAID channel.

Claim 20 (original): The article of claim 17, wherein the storage medium further comprises machine-readable instructions stored thereon for initiating a bus transaction to set information in a configuration header maintained at the peripheral device to conceal the first device function from subsequent enumeration procedures.

Claim 21 (previously presented): A processing system comprising:

logic to initiate a first enumeration procedure to enumerate a first device function defined by a peripheral device coupled to a data interface of a data bus, the peripheral device defining a plurality of device functions; and

logic to initiate a bus transaction on the data bus to cause the first device function to be concealed from subsequent enumeration procedures while enabling the subsequent enumeration procedures to access at least one other device function defined by the peripheral device.

Claim 22 (original): The processing system of claim 21, the processing system further comprising logic to enumerate the first device function as an I/O channel.

Claim 23 (original): The processing system of claim 22, wherein the device function associated with the I/O channel comprises logic to communicate with a RAID channel.

Claim 24 (currently amended): The processing system of claim 21, wherein the processing system further comprises logic to initiate a bus transaction to set information in a configuration header maintained at the peripheral device to conceal the first device function from subsequent enumeration procedures while enabling the processing system to communicate with the first device function.

Claim 25 (original): The processing system of claim 24, wherein the data bus comprises a PCI data bus and the processing system further comprises logic to initiate a bus transaction to modify data in a Header Type register of the configuration header.

Claim 26 (previously presented): A method comprising:
initiating a first enumeration procedure at a first processing system to enumerate a first device function defined by a peripheral device coupled to a data interface of a data bus, the peripheral device defining a plurality of device functions; and

initiating a bus transaction on the data bus to cause the first device function to be concealed from subsequent enumeration procedures while enabling at least one other device function defined by the peripheral device to be enumerated by a subsequent enumeration procedure.

Claim 27 (previously presented): The method of claim 26, the method further comprising enumerating the first device function as an I/O channel.

Claim 28 (previously presented): The method of claim 27, wherein the device function associated with the I/O channel comprises logic to communicate with a RAID channel.

Claim 29 (currently amended): The method of claim 26, the method further comprising initiating a bus transaction to set information in a configuration header maintained at the peripheral device to conceal the first device function from subsequent enumeration procedures while enabling the first processing system to communicate with the first device function.